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11/07/2003

STAAS & HALSEY LLP
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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/753,570 | 01/04/2001 | Shunji Baba | 1381.1005 | 2008 |

TITLE OF INVENTION: METHOD OF MOUNTING CHIP ONTO PRINTED CIRCUIT BOARD IN SHORTENED WORKING TIME

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 02/09/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|--------------|----------|----------------|
| ROMAN, ANGEL | 2812 | 438-106000 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 STAAS & HALSEY LLP

2 _____

3 _____

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(A) NAME OF ASSIGNEE

FUJITSU LIMITED

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

(Date)

H. J. Staas, Reg. No. 22,010

Dec. 22, 2003

12/30/2003 AWONDAF2 00000099 09753570

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